

FIG. 1

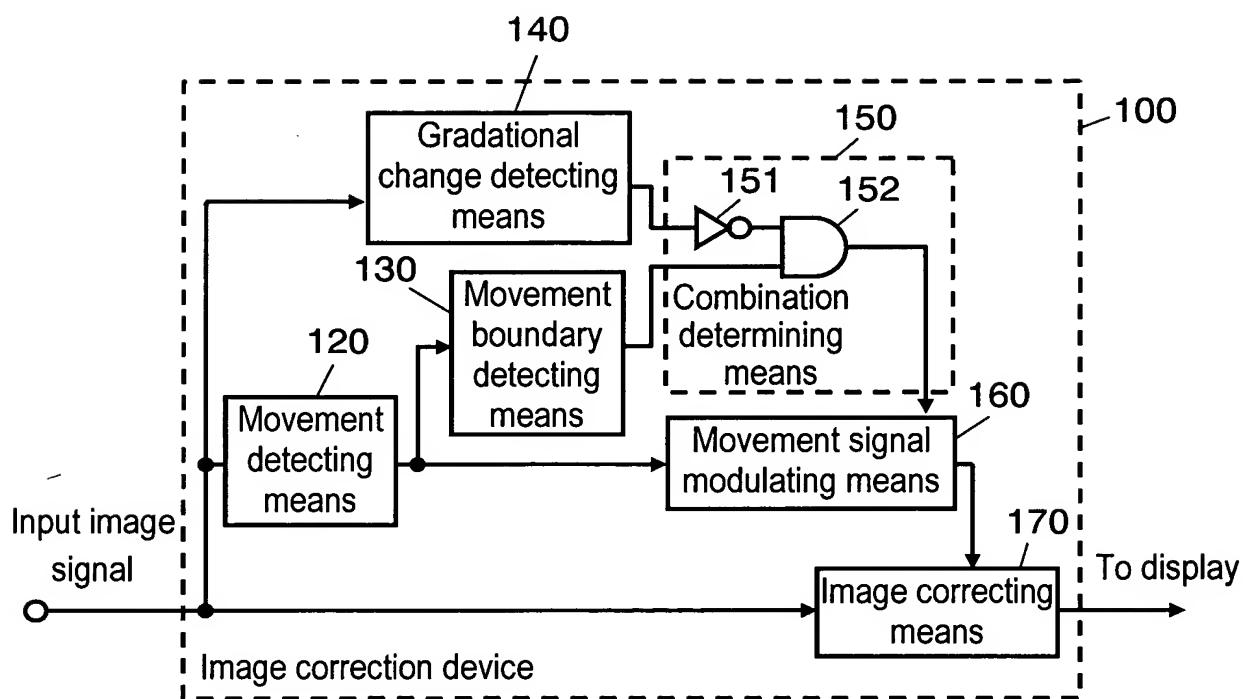
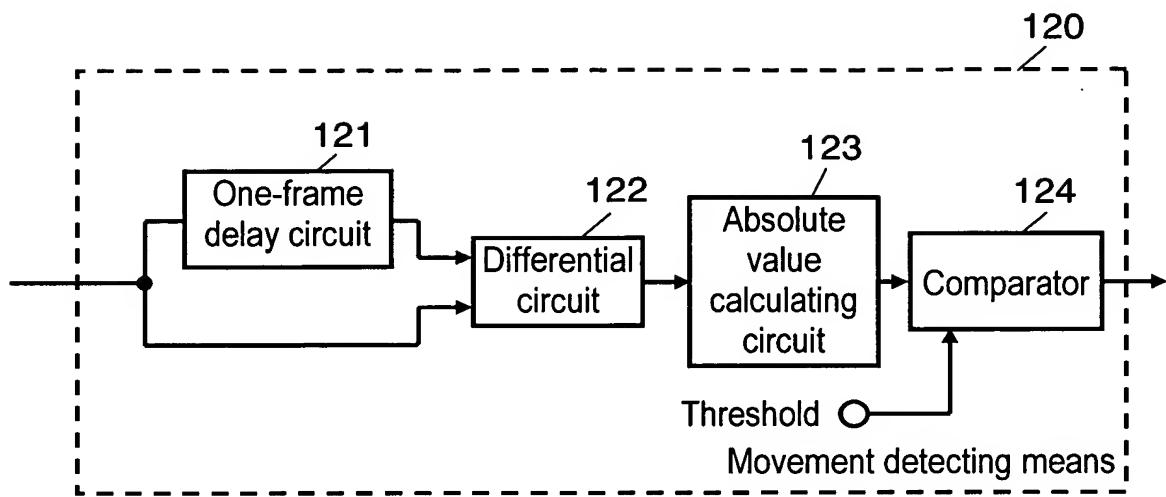


FIG. 2



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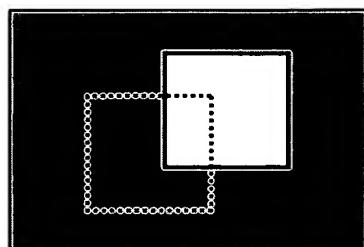
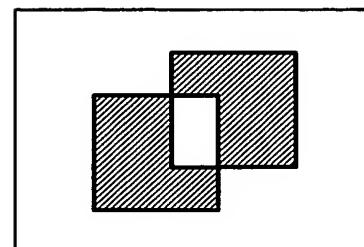
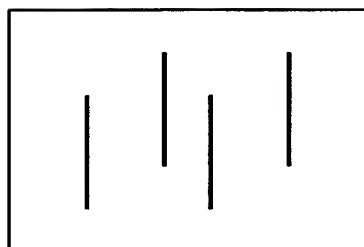
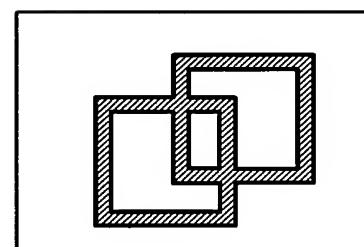
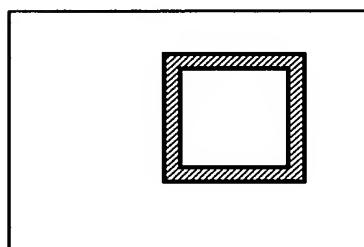
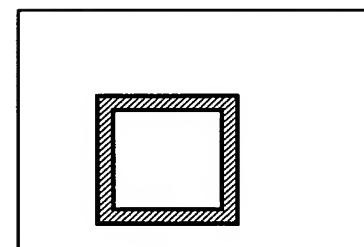
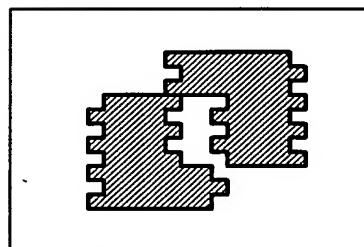
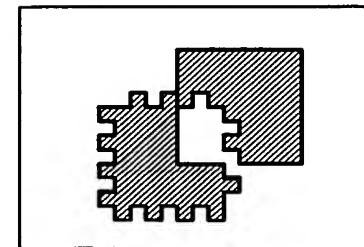
FIG. 3A**FIG. 3B****FIG. 3C****FIG. 3D****FIG. 3E****FIG. 3F****FIG. 3G****FIG. 3H**

FIG. 4

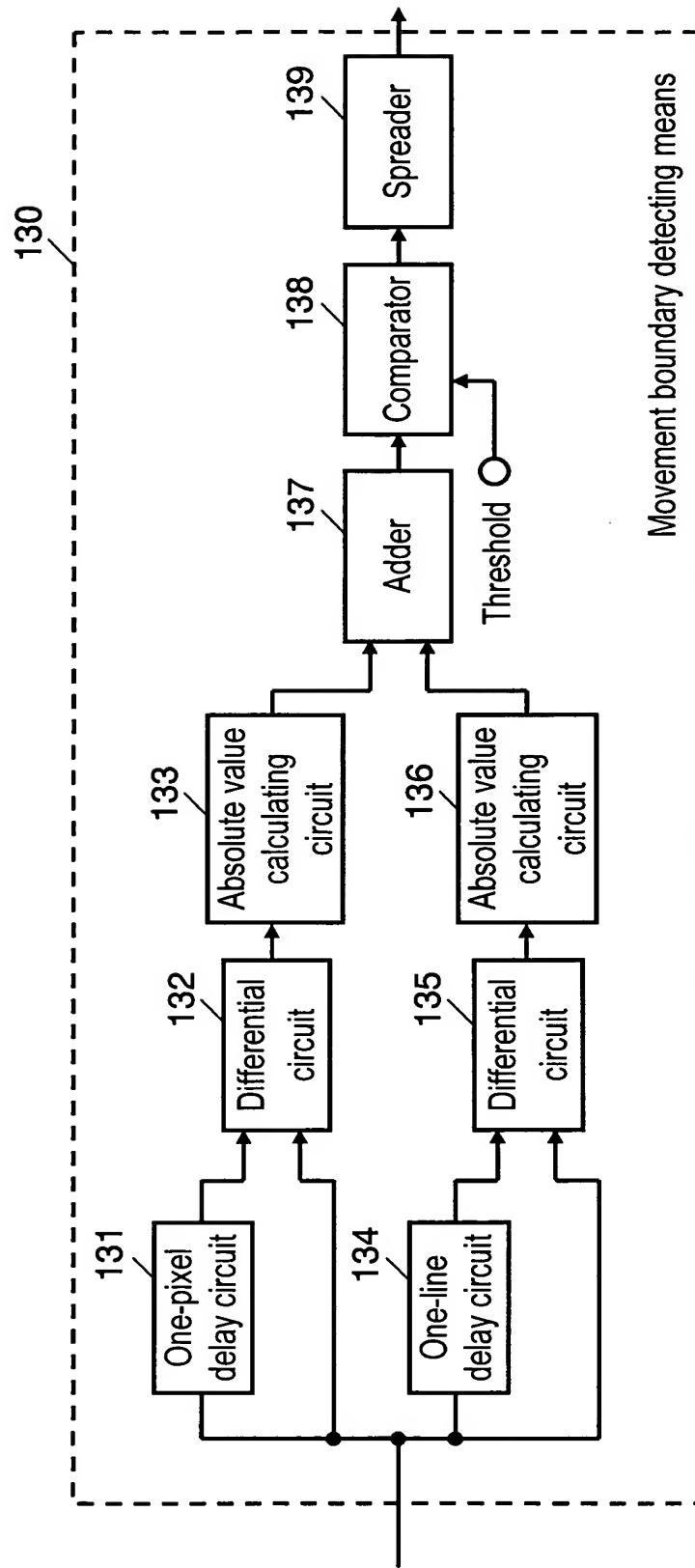


FIG. 5

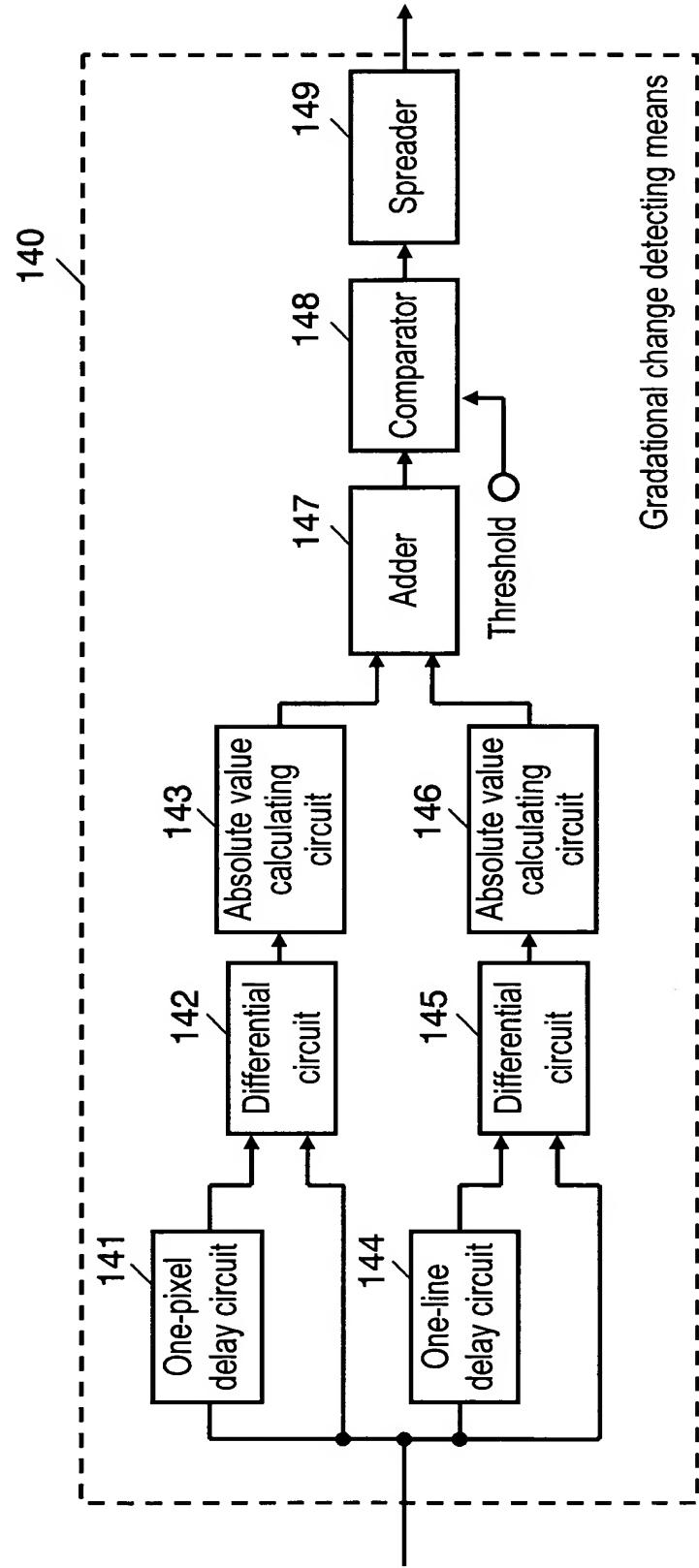


FIG. 6

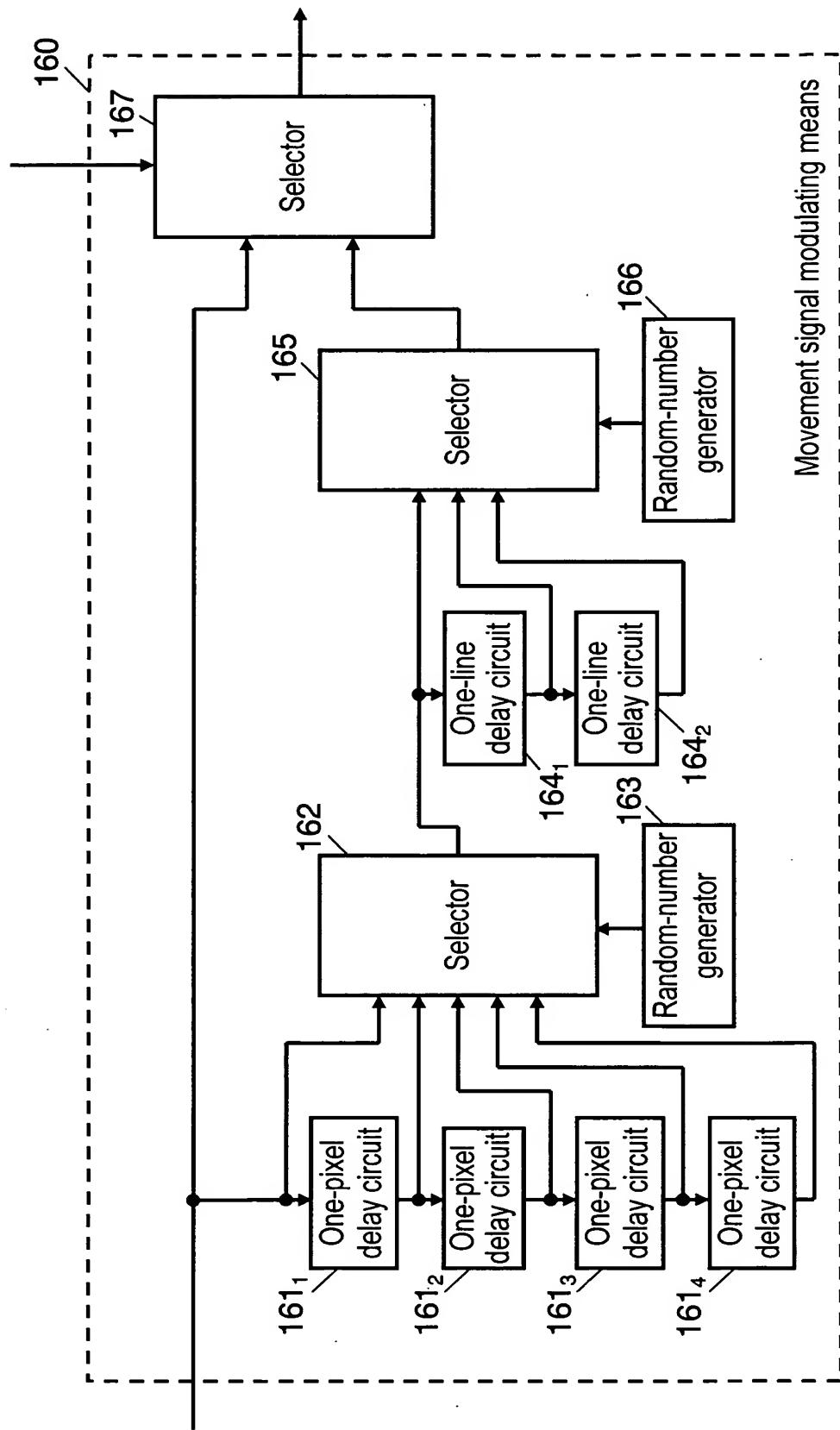


FIG. 7

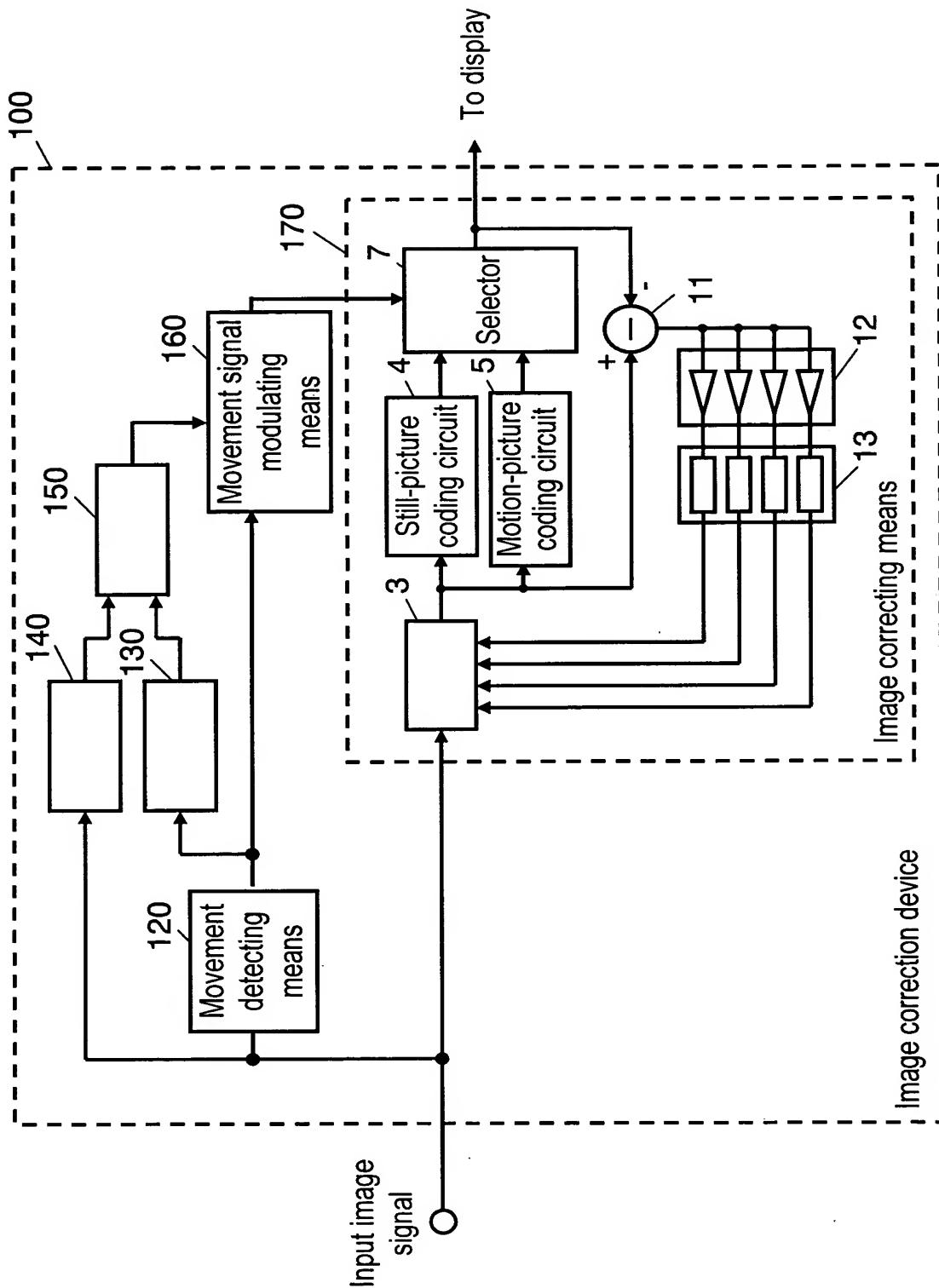
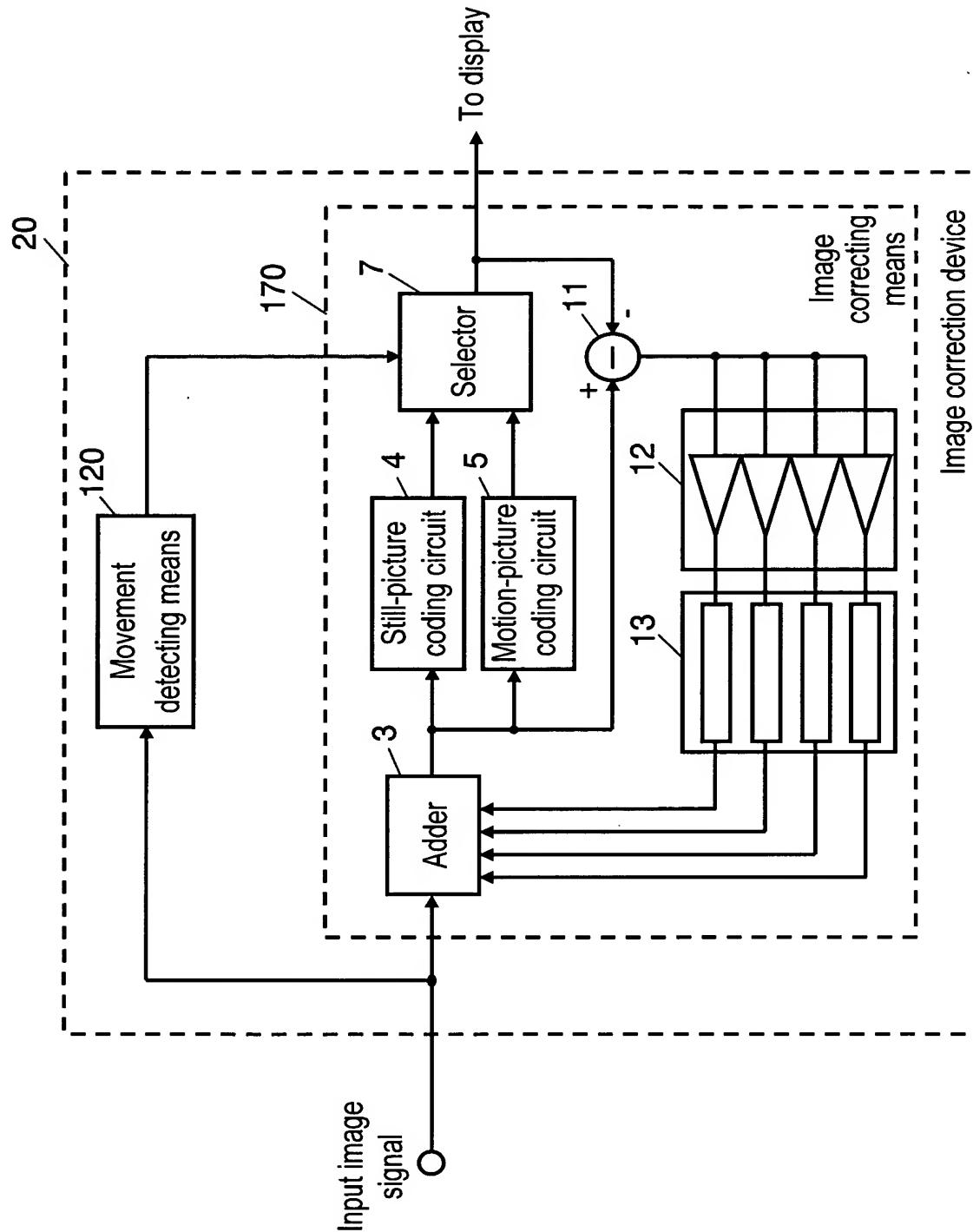


FIG. 8



Reference numerals in the drawings

- 100 image correction device
- 120 movement detecting means
- 121 one-frame delay circuit
- 122, 132, 135, 142, 145 differential circuit
- 123, 133, 136, 143, 146 absolute value calculating circuit
- 124, 138, 148 comparator
- 130 movement boundary detecting means
- 131, 141, 161₁ – 161₄ one-pixel delay circuit
- 134, 144, 164₁ – 164₂ one-line delay circuit
- 137, 147 adder
- 139, 149 spreader
- 140 gradational change detecting means
- 150 combination determining means
- 151 NOT-circuit
- 152 AND-circuit
- 160 movement signal modulating means
- 162, 165, 167 selector
- 163, 166 random-number generator
- 170 image correcting means